APPLICANTS:

Alon RAM et al.

SERIAL NO.:

10/032,148

FILED: ASSIGNEE: December 31, 2001 Intel Corporation

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Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims

Claims 1 - 16 (Cancelled)

Claim 17. (New) A mobile unit comprising:

a global positioning system (GPS) receiver to receive global positioning signals from global positioning satellites;

a communication transceiver to communicate with a base station; and

an integrated circuit including a first processor to calculate, at least in part, a location of said mobile unit from at least said global positioning signals and to process base station signals received by said communication transceiver from said base station and to provide said communication transceiver with signals to be transmitted to said base station.

Claim 18. (New) The mobile unit of claim 17, wherein said communication transceiver is a code division multiple access (CDMA) transceiver.

Claim 19. (New) The mobile unit of claim 17, wherein said communication transceiver is a time division multiple access (TDMA) transceiver.

Claim 20. (New) The mobile unit of claim 17, wherein said processor is a digital signal processor (DSP).

Claim 21. (New) The mobile unit of claim 17, wherein said processor is a controller.

Claim 22. (New) The mobile unit of claim 17, further comprising:

an andio apparatus; and

a memory,

wherein said memory is coupled to said integrated circuit through a bus and said audio apparatus is coupled to said integrated oircuit through at least said bus.

PAGE 4/9 * RCYD AT 11/2/2004 9:56:09 AM [Eastern Standard Time] * SVR:USPTO-EFXRF-2/0 * DNIS:7469798 * CSID:972 9 9574933 * DURATION (mm-ss):03-24

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Claim 23. (New) The mobile unit of claim 22, further comprising.

a buffer memory to couple said audio apparatus to said integrated circuit.

Claim 24. (New) The mobile unit of claim 17, further comprising:

a hardware accelerator coupled to said processor, wherein said hardware accelerator is to cooperate with said processor in processing said base station signals and in providing said communication transceiver with said signals to be transmitted to said base station.

Claim 25. (New) The mobils unit of claim 24, wherein said hardware accelerator is to cooperate with said processor in calculating, at least in part, said location of said mobile unit.

Claim 26. (New) The mobile unit of claim 17, further comprising:

a second processor coupled to said first processor.

wherein said second processor is to cooperate with said first processor in processing said base station signals and in providing said communication transceiver with said signals to be transmitted to said base station.

Claim 27. (New) The mobile unit of claim 26, wherein said second processor is to cooperate with said first processor in calculating, at least in part, said location of said mobile unit.

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